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Date:

March 3, 2006

To:

United States Patent and Trademark Office

Examiner: Chen, Kin Chan; Art Unit: 1765

Fax:

(571) 273-8300

Re:

Application Serial No.: 10/705,347

Filing Date: 11/8/2003; First-Named Inventor: Labelle

Attorney Docket No.: 0180151

From:

Farjami & Farjami LLP

Number of pages including the cover sheet: 4

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on 3/3/06	For: Method for Integrating a High-K Gate Dielectric in a Transistor Fabrication Process				
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x attorney or agent of record. Registration number 38,135	(949) 282-1000 Telephone number				
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